

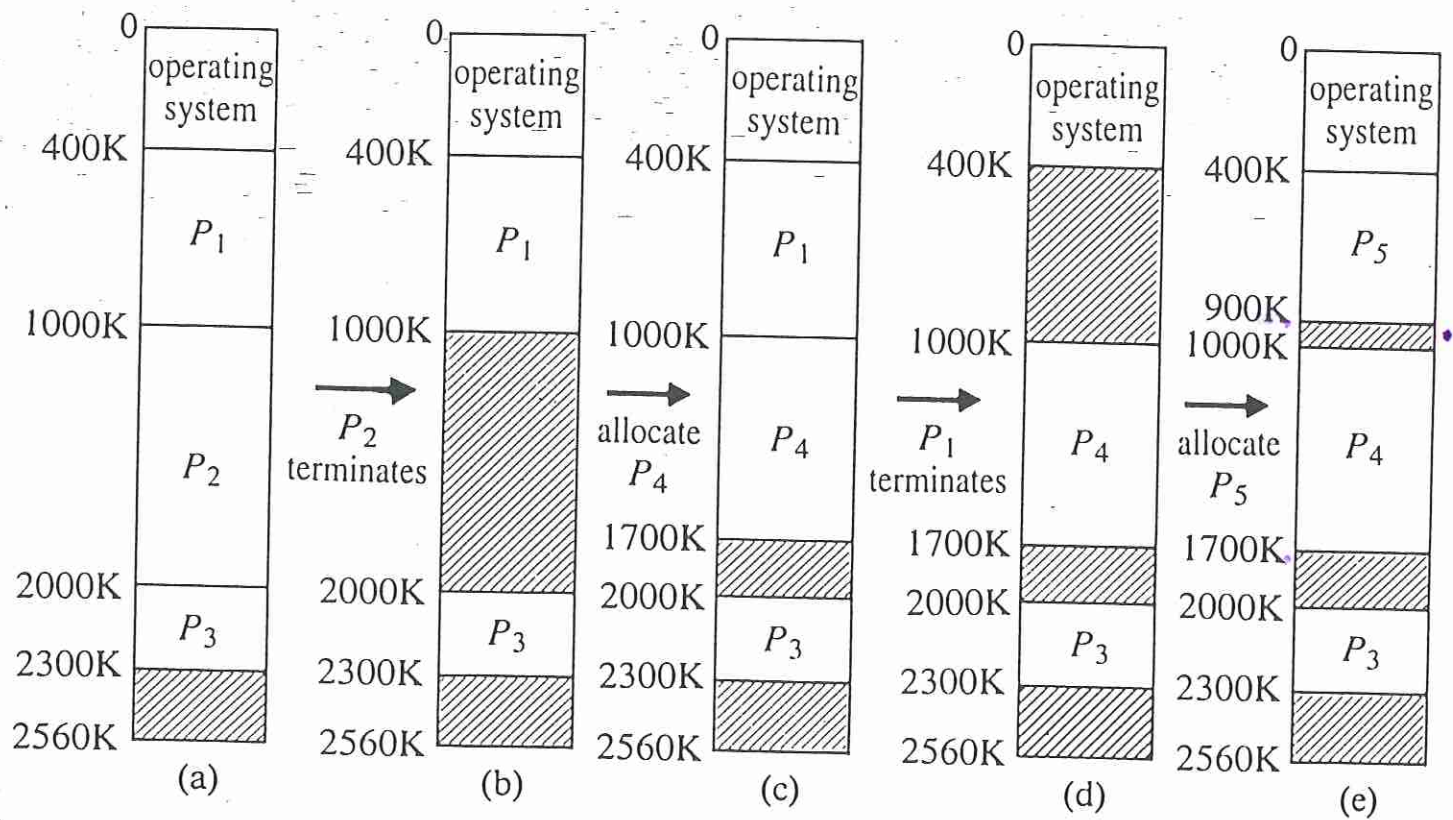


Figure 7.8 Memory allocation and long-term scheduling.

Log. addr. **773**

$773/256 = 3 \text{ rem } 5$

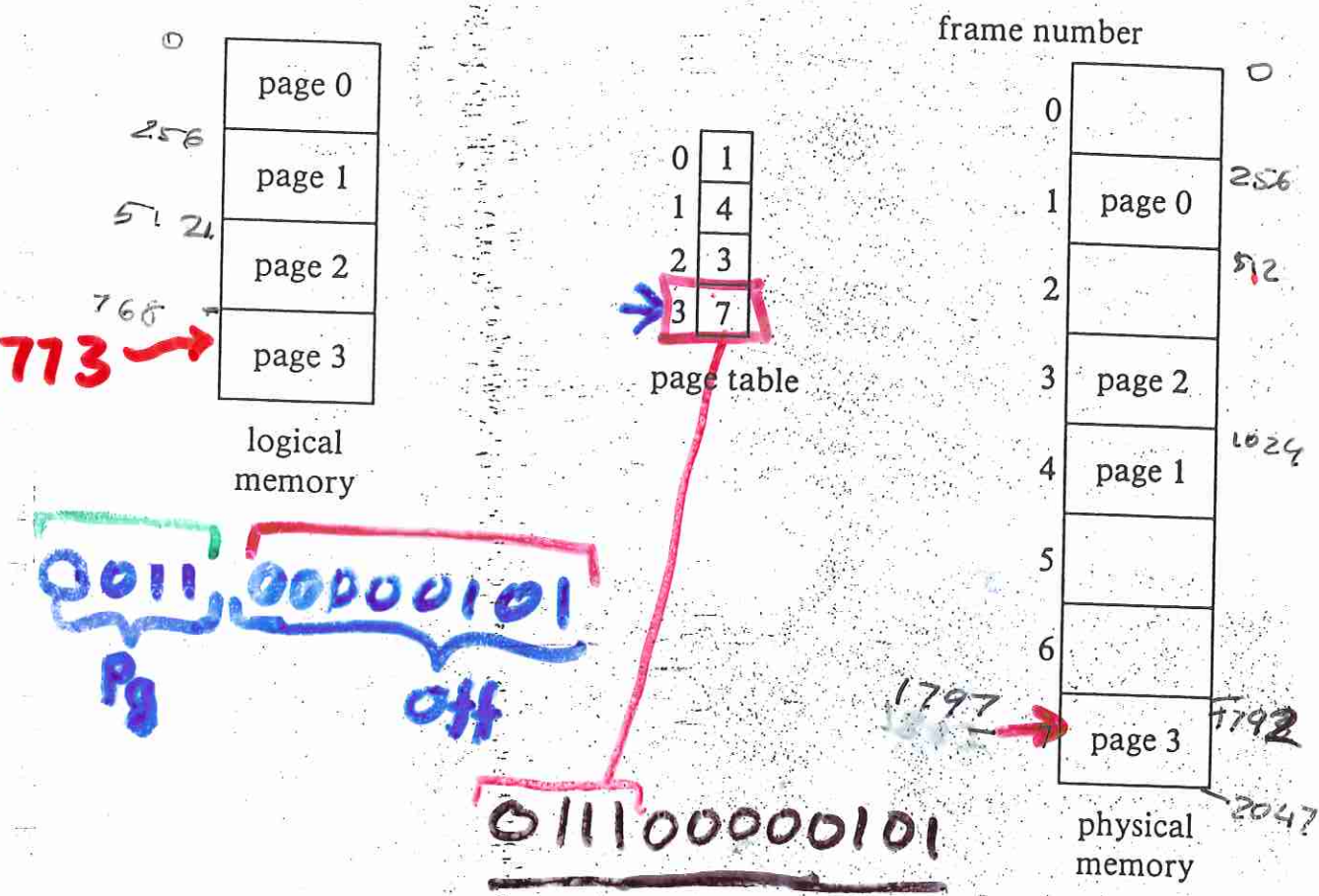


Figure 7.14 Paging model of logical and physical memory.

3	5
---	---

 Logical
 address

7	5
---	---

 Physical
 address